

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Michael K. Gschwind et al.

Serial No.:

Group Art Unit:

Filed:

Herewith

Examiner:

For:

Processor Implementation having

Unified Scalar and SIMD Datapath

Docket No: YOR920010675

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In order to assist in the examination of this application, and in accordance and compliance with 37 CFR 1.56, 1.97 and 1.98, a copy of each of the documents listed on the attached form PTO-1449 is forwarded herewith.

Respectfully submitted,

Bv:

Robert P. Tassinari, J

Registration No. 36,030

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